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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	09/843,630
(use as many sheets as necessary)		Filing Date	April 27, 2001
		First Named Inventor	Kazuo Nishiyama
		Group Art Unit	2813
		Examiner Name	James M. Mitchell
		Attorney Docket Number	075834.00074
Sheet	/	of	/

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
/Jmm/	/	Wolf, Silicon Processing for the VLSI Era, Volume 2:Process Integration, 1990, pp 334, 335 & 337	

Examiner Signature	/James Mitchell/	Date Considered	05/01/2007
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Notice of References Cited			Application/Control No.	Applicant(s)/Patent Under Reexamination	
			09/843,630	NISHIYAMA, KAZUO	
Examiner James M. Mitchell			Art Unit 2813	Page 1 of 1	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,271,469	08-2001	Ma et al.	174/521
*	B	US-6,825,569	11-2004	Jiang et al.	257/783
*	C	US-6,306,380	10-2001	Desmots et al.	424/73
*	D	US-6,117,704	09-2000	Yamaguchi et al.	438/100
*	E	US-5,841,193	11-1998	Eichelberger, Charles William	257/723
*	F	US-5,048,179	09-1991	Shindo et al.	29/840
*	G	US-6,452,265	09-2002	Furukawa et al.	257/723
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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